16. The patch panel assembly of claim 15, wherein the first and second cover portions at least partially overlap each other to partially enclose the primary and secondary circuit boards in the hollow nest, the first and second cover portions engaging each other where they overlap.

17. The patch panel assembly of claim 13, wherein the first connectors further include:

jacks, each jack including a housing with an interior receptacle disposed therein, the jack interior receptacle including a plurality of conductive terminals disposed therein, the first connector receptacle opening being disposed in a forward face of the jack; and

rear termination portions, the rear termination portions including a plurality of conductive terminals for terminating network individual wires thereto, the housing and rear termination portion terminals being connected together such that a cable connected to one jack may be connected to at least one work area outlet, the first connector receptacle openings facing the face panel.

18. The patch panel assembly of claim 17, wherein the housing is arrayed along the primary circuit board first surface in a linear array, and the hollow nest is disposed underneath the housing.

19. The patch panel assembly of claim 18, wherein the visual indicators include a plurality of light-emitting diodes arranged on the primary circuit board, disposed beneath the first connectors.

20. The patch panel assembly of claim 13, wherein the second connector is disposed on the secondary circuit board and extending therefrom through the hollow nest.

21. The patch panel assembly of claim 20, wherein the second connector includes a receptacle opening, the receptacle opening facing a direction opposite the first connector receptacle openings.

22. A patch panel for use in a data network, the patch panel comprising:

first and second elongated circuit boards, the second circuit board being smaller than the first circuit board, the first circuit board being mounted to the second circuit board by spacers so that the first and second circuit boards are spaced apart from each other, lengthwise, by an intervening space, the first and second circuit boards including respective first and second mounting surfaces that confront each other lengthwise and define sidewalls of the intervening space;

a plurality of electronic elements, each electronic element being supported by at least one of the first and second mounting surfaces;

a plurality of first connectors, the first connectors being supported by the first circuit board on the first mounting surface and extending away from the first mounting surface and over the intervening space; and

at least one second connector supported by the second circuit board on the second mounting surface.

23. The patch panel of claim 22, wherein the electronic elements include first and second integrated circuit members respectively disposed on the first and second mounting surfaces.

24. The patch panel of claim 22, where the first connectors also extend over an edge of the second circuit board that confronts the first connectors.

25. The patch panel assembly of claim 22, wherein the second connector extends through the intervening space and through a notch disposed in the first circuit board.

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